IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Huili Shao Susan Vitkavage Allen Yen

Case:

1-10-31

Serial No.:

09/464,811

Group Art Unit:

2815

I hereby certify that this correspondence is being faxed to the United States Postal Service at (703) 872-9318 on November

27, 2002 and mailed first class mail in an envelope addressed

to: Commissioner of Pagents and Trademarks Washington,

Filed:

December 17, 1999

Examiner:

L. Cruz

Title:

Integration of Low Dielectric Material in Semiconductor Circuit S

Structures

AMENDMENT

Assistant Commissioner of Patents Washington, D.C. 20231

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SIR:

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In response to the office action of November 1, 2002, please amend the aboveidentified application as follows:

IN THE CLAIMS

22. (Amended) A method for forming a semiconductor structure, comprising:

depositing a first insulative layer over a semiconductor layer; forming a lower level of interconnect members over the first insulative layer; depositing a second insulative layer between and over the lower level of interconnect members;

forming an upper level of interconnect members over the second insulative layer removing portions of the second insulative layer positioned between interconnect members of the lower and upper levels; and

forming a third insulative layer in regions from which the second insulative layer is removed.

agere systems

Formerly the Microelectronics Group of Lucent Technologies 9333 South John Young Parkway Orlando, Fl. 32819-8698

INTELLECTUAL PROPERTY LAW FACSIMILE TRANSMITTAL SHEET	
To: V5 PTO	From: Fred Romano
Fax: 703-872-9318	Pages: 식 (Including Cover Sheet)
Re: 09/464;811	Date: 11 27 07.
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